



Ethernet Routing Switch 8600

Readme

Software Release 7.1.3.2



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Software Release 7.1.3.2

Release Date: May 8, 2012

Purpose: Software Patch release to address externally found customer issues.

Important Notices

Supported hardware for Release 7.1.3.2:

Refer to the ERS 8800 Release Notes for a complete list of supported hardware - **Release Notes — Software Release 7.1.3 Avaya Ethernet Routing Switch 8800/8600 (NN46205-402, 08.02)** available at <https://support.avaya.com>

Upgrade Notes

This section describes the Ethernet Routing Switch 8800 Software Release 7.1.3.2 software files.

Before you upgrade, it is recommended to verify the MD5 signature for each new file to be used.

For upgrade procedures, see **Upgrades — Software Release 7.1.3 Avaya Ethernet Routing Switch 8800/8600 (NN46205-400, 07.03)** available at <https://support.avaya.com>

File Names for This Release

Module or file Type	Description	File name	Size in bytes
Software tar file Tar file of all software	Deliverables (includes images that also contain encryption software)	pr86_7132.tar.gz	67663299
Ethernet Routing Switch images			
Boot monitor image for 8692 SF/CPU	8692 CPU and switch fabric firmware	p80b7132.img	1188255
Boot monitor image for 8895 SF/CPU	8895 CPU and switch fabric firmware	p80be7132.img	1254624
Run-time image for 8692 SF/CPU	Run-time image for 8692 SF/CPU	p80a7132.img	15797821
Run-time image for 8895 SF/CPU	Run-time image for 8692 SF/CPU	p80ae7132.img	14828460
Run-time image for R modules	Run-time image for R modules	p80j7132.dld	1790884
Run-time image for RS modules	Run-time image for RS modules	p80k7132.dld	1857672



Run-time image for Enterprise Enhanced SF/CPU Daughter Card (SuperMezz)	Image for the SuperMezz Card	p80m7132.img	15876174
3DES for 8692 SF/CPU	Encryption module for privacy protocol with Secure Shell (SSH)	p80c7132.des	56124
3DES for 8895 SF/CPU	Encryption module for privacy protocol with Secure Shell (SSH)	p80ce7132.des	51972
AES for 8692 SF/CPU	Encryption module for privacy protocol with Secure Shell (SSH)	p80c7132.aes (this image includes the 3DES image)	27436
AES for 8895 SF/CPU	Encryption module for privacy protocol with Secure Shell (SSH)	p80ce7132.aes (this image includes the 3DES image)	25156
MIB	MIB files	p80a7132.mib	5242683
MIB (zip file)	Zip file containing MIBs	p80a7132.mib.zip	821181
MD5 checksum File	md5 checksums of all Release 7.0 software files	p80a7132.md5	1230
FOQ for R modules	Feedback output queuing FPGA firmware	foq267.xsvf	5320469
BMC for R modules	BAP memory controller FPGA firmware	bmc776.xsvf	2640266
DPC for R modules	Dual port Controller FPGA firmware	dpc184.xsvf	2642001
PIM8630GBR	Programmable I/O module FPGA firmware; for the 8630GBR only	PI_769.xsvf	2284578
Firmware for RS modules	Contains FOQ, BMC, DPC, mirroring, and loopback images	rs_dpm_fpga.bin	4538368
PIM images for RS modules	PIM FPGA firmware required for 8612XLRS module only	pim8612XLRS.bin	60183
	PIM FPGA firmware required for 8634XGRS module only	pim8634XGRS.bin	78173



	PIM FPGA firmware required for 8648GBRS module only	pim8648GBRS.bin	79891
	PIM FPGA firmware required for 8648GTRS module only	pim8648GTRS.bin	54441
Trace files			
MPLS trace file	Trace file for MPLS. This is auto generated and appears on the PCMCIA after upgrade.	nbpdtrc.lo0	
EDM Help files			
EDM help files	Help files for EDM GUI	Ers8000v7132_HELP_EDM.zip	5095300
ERS 8000 EDM plug-in for COM			
EDM plug-in for COM	EDM plug-in for COM	ers8000v7.1.3.2.war	7444280

NOTE: dpc194.xsvf is a new DPC FPGA image for R-modules that can only be used with 7.0.0.1 or greater releases. These are not packaged as part of the release. Please obtain the software from Avaya Support if needed.

Version of Previous Release

Software Version **7.1.3.1**

Compatibility

This software release is managed with Enterprise Device Manager (EDM)

Changes in This Release

New Features in This Release

None.

Old Features Removed From This Release

None.

Problems Resolved in This Release

Every 5 seconds, on a timer, the CPU sends the clock time to all the line cards. A timer was created each time a card came online, so when there are multiple IO cards, multiple messages were sent every 5 seconds to all cards. Eventually a lockup resulted, and when it was detected by the CPU, the chassis was reset. [wi00996291]



Outstanding Issues

Known Limitations

When we are running multiple ports configured in the same lane, we see momentary traffic loss on other ports in the lane, when the operating speed/duplex changes on one of the ports. If the operating speed/duplex remains same as before, there is no traffic loss. For example, if we pull a SFP and plug it back in, and the speed/duplex remains same as before, we will not see traffic loss. The reason for this is that every time a port speed changes, we need to re-configure lane specific shapers. This is the case if we are re-configuring Egress Queue Sets and configuring port shapers also.

Please refer to the Known Limitations Section of the Release Notes for Ethernet Routing Switch 8600 Software Release 7.1.3.0. No other new known limitations have been found.

Documentation Corrections

None.



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